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INFORMATION DISCLOSURE STATEMENT

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Applicant: Shunpei YAMAZAKI et al.

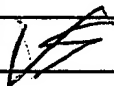
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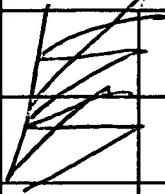
U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date (if appropriate)

FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Subclass	Translation Yes No
	7-162004	6/23/95	Japan			Abstract

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	"Recent Progress of Low Temperature poly-Si Technology" Hiroshi TSUTSU, Proceedings of Electronic Display Forum, 99, pp. 4-31 to 4-38 (Full translation attached)
	"Uniformity Improvement of Excimer Laser Crystallized Poly-Si Thin Film Transistors" Sera et al., AM-LCD '96/IDW '96, 1996, pp. 85-88.

Examiner

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